

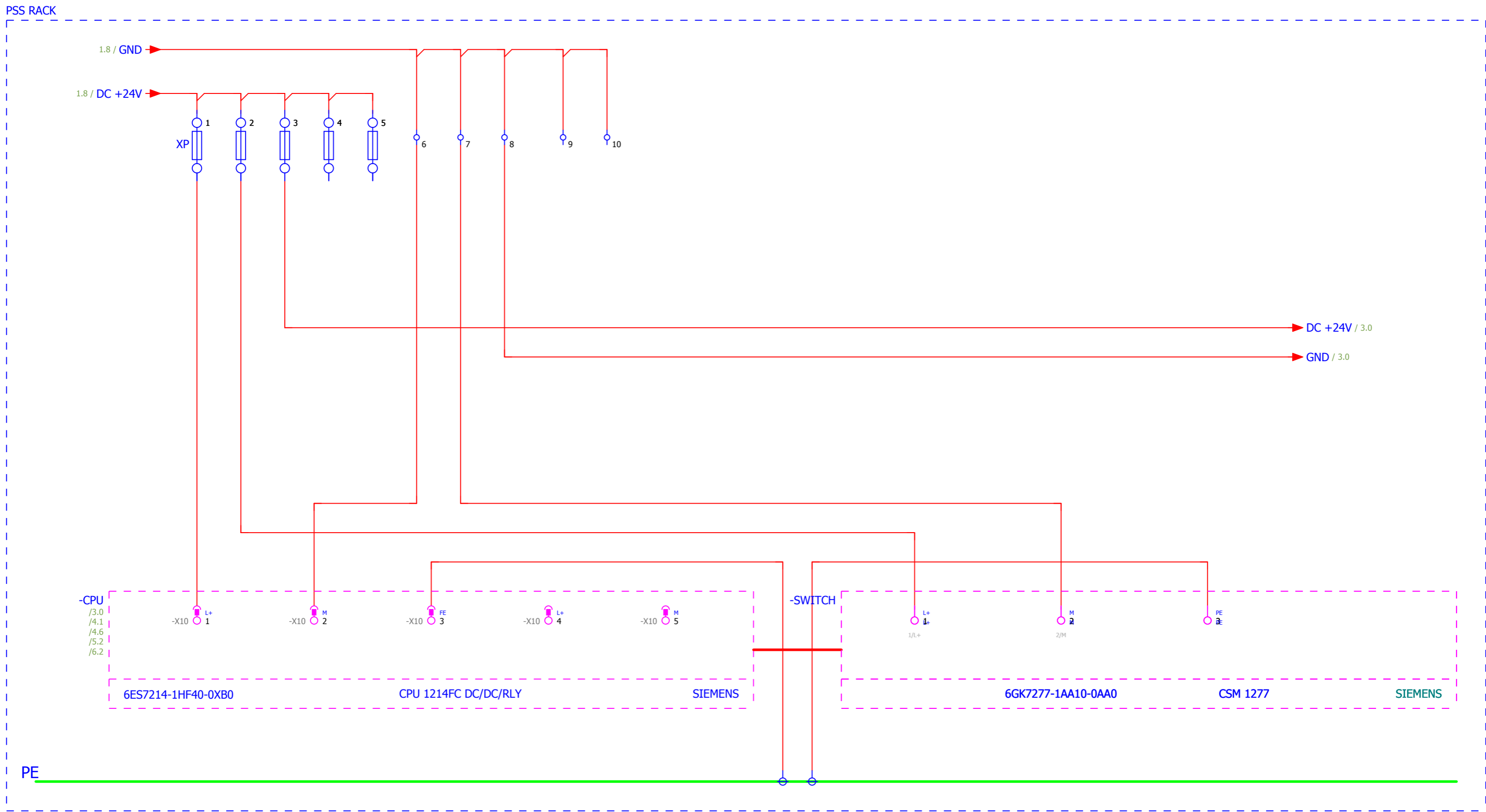
Dátum	2017.10.18.
Szerk..	Standard
Lenyomva	
Módosítás	Dátum
Név	
eredetileg	

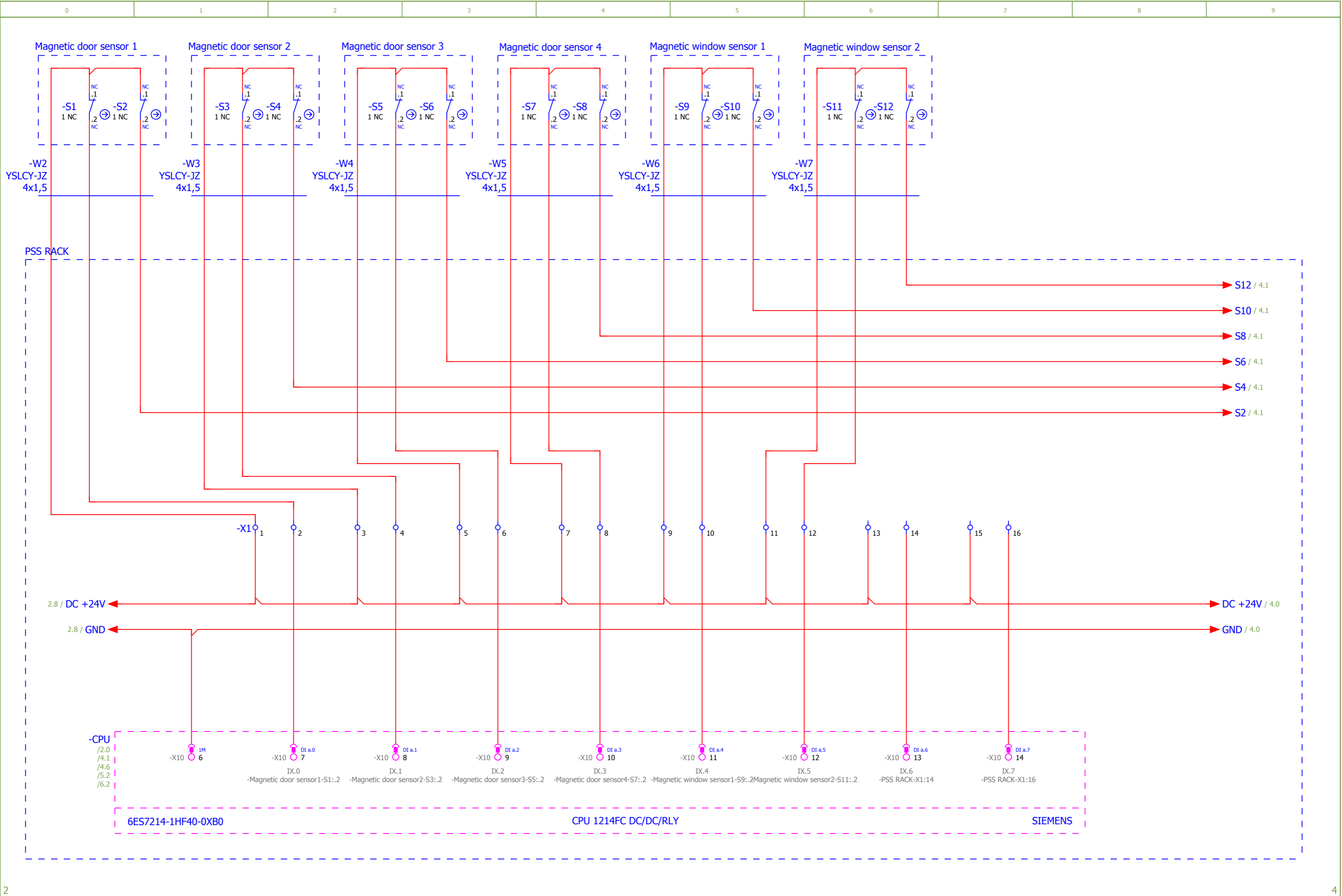
PSS HR	
Kiegészítette	Pótolta



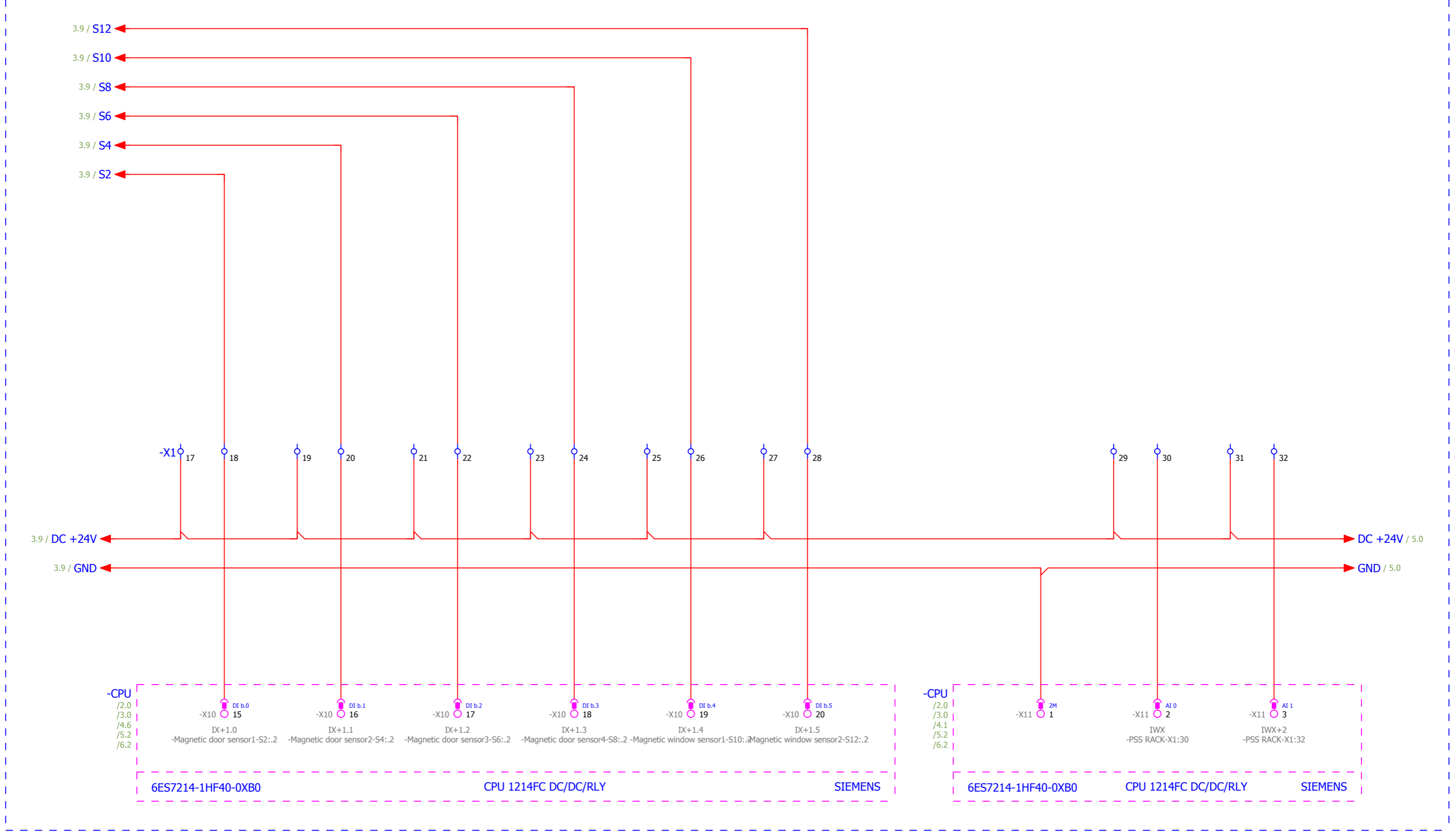
DC POWER SUPPLY

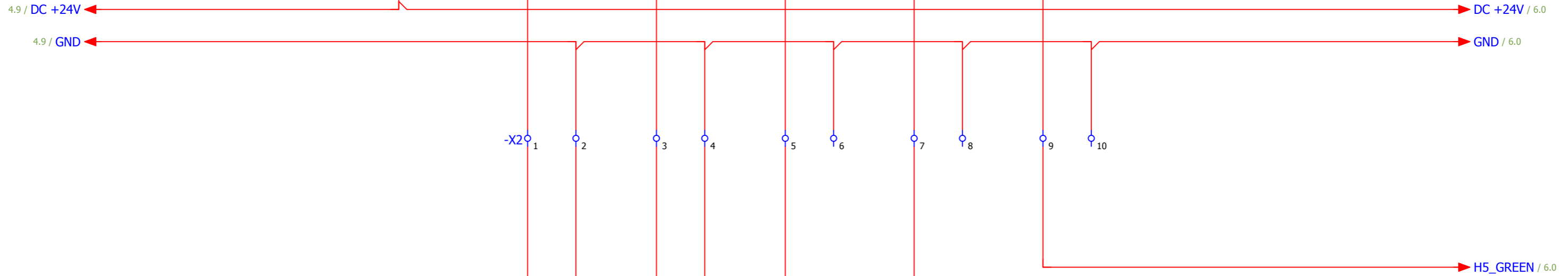
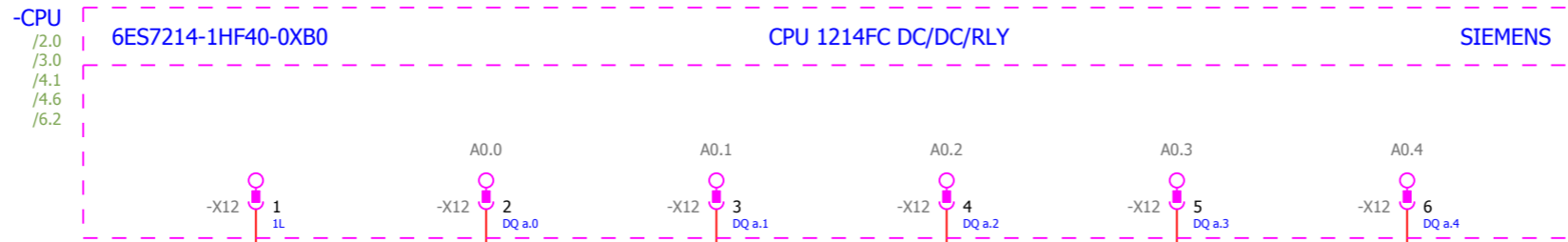
=	
+	
ELI_EE_2017	Lap 1 / 19
	Tervlap 1 / 19





PSS RACK

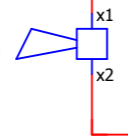




-W22
YSLCY-JZ
4x1,5

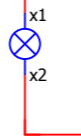
-W23
YSLCY-JZ
7x1,5

-H1



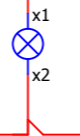
-H2

GREEN



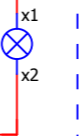
-H3

YELLOW



-H4

RED



Visual sign 1

-CPU 6ES7214-1HF40-0XB0 CPU 1214FC DC/DC/RLY SIEMENS

A0.5 A0.6 A0.7 A1.0 A1.1
 -X12 1 1L -X12 2 DQ a.0 -X12 3 DQ a.1 -X12 4 DQ a.2 -X12 5 DQ a.3 -X12 6 DQ a.4

5.8 / DC +24V ← → DC +24V / 7.0

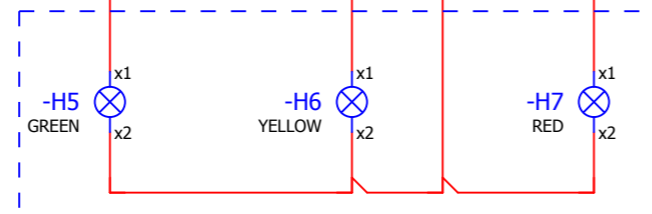
5.8 / GND ← → GND / 7.0

5.8 / H5_GREEN ←

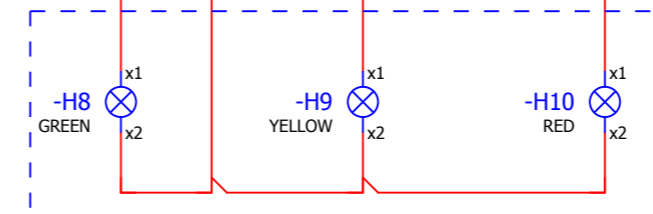
-X2 11 12 13 14 15 16 17 18 19 20

-W24
YSLCY-JZ
7x1,5

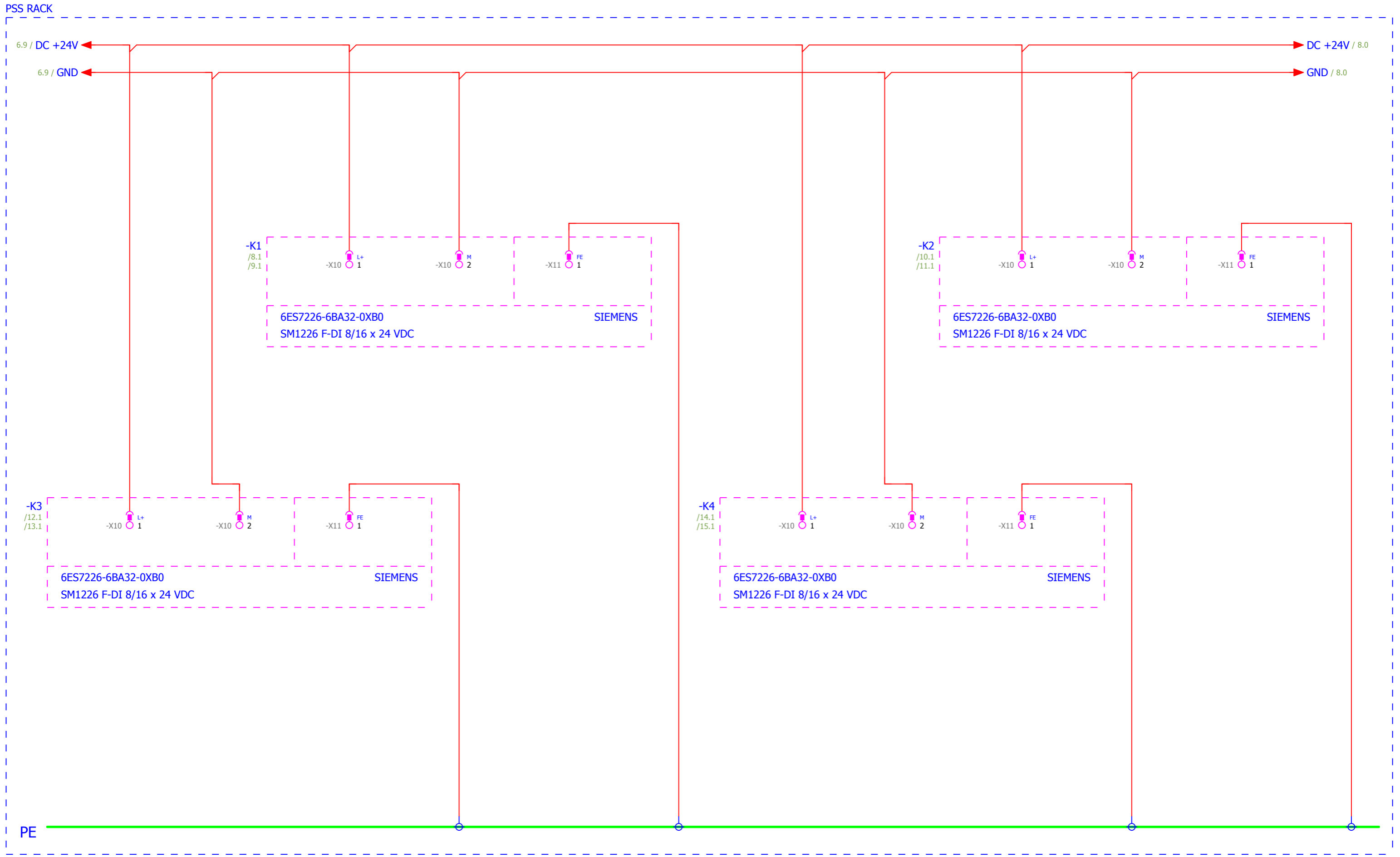
-W25
YSLCY-JZ
7x1,5



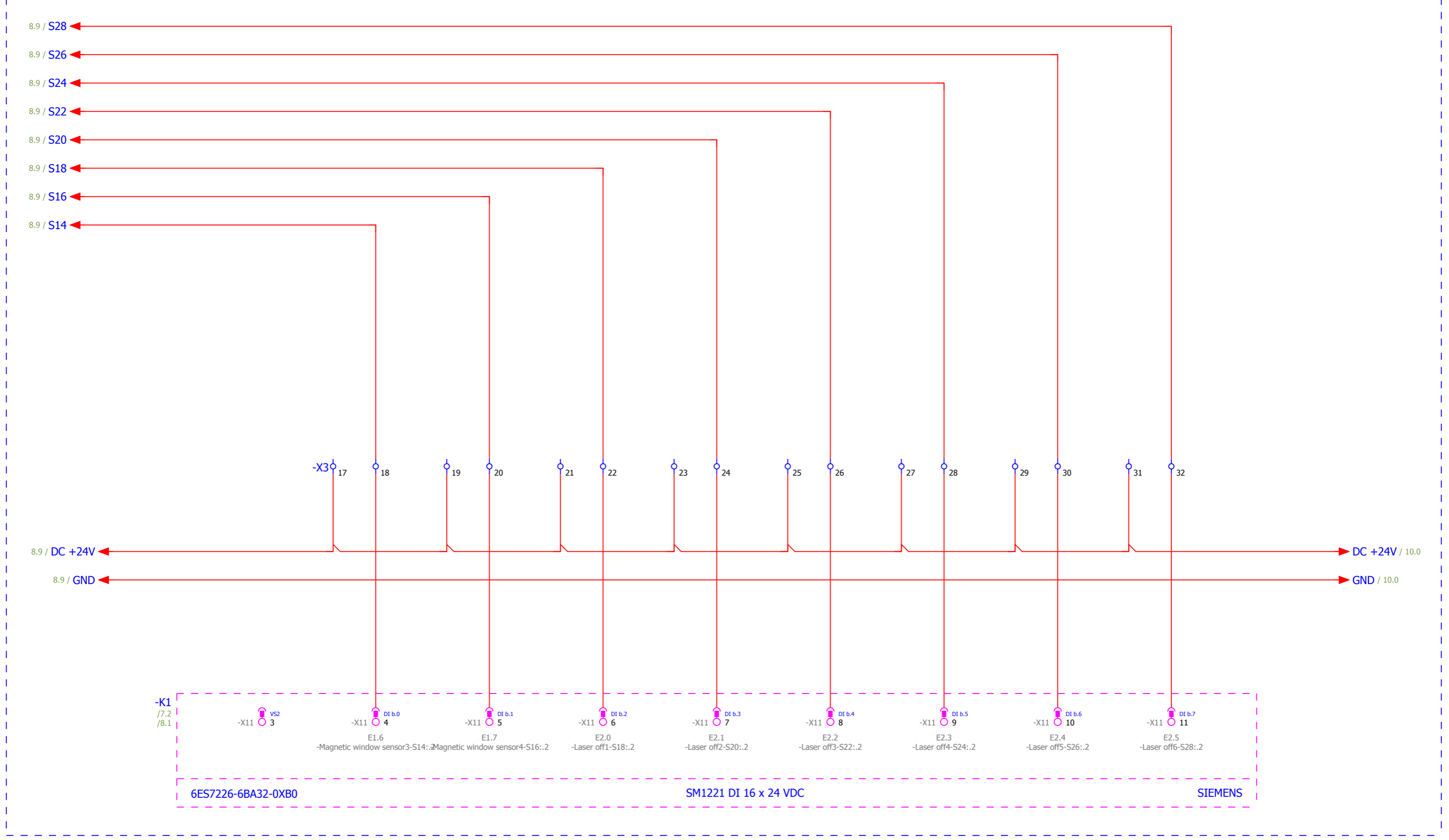
Visual sign 2



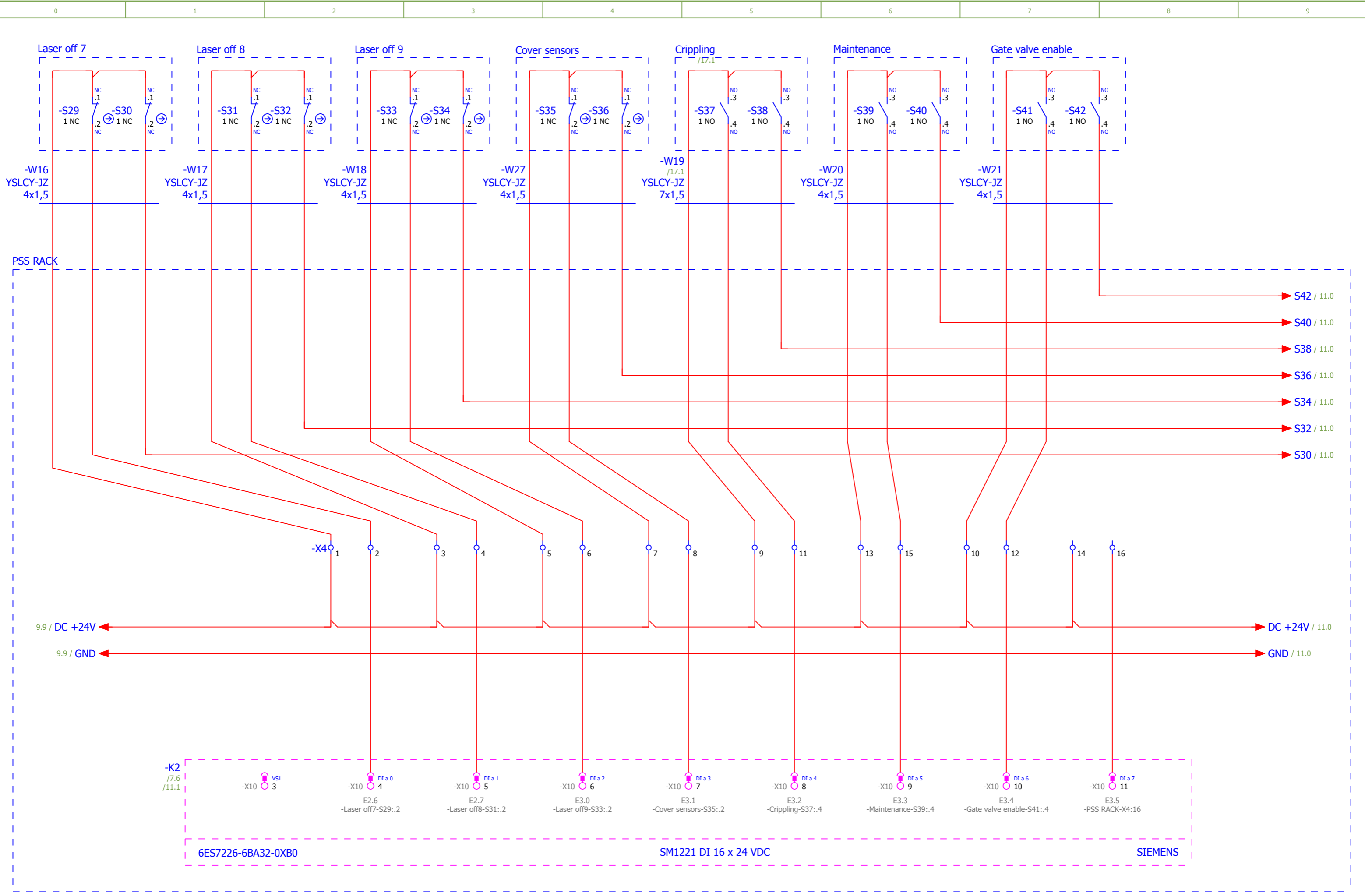
Visual sign 3



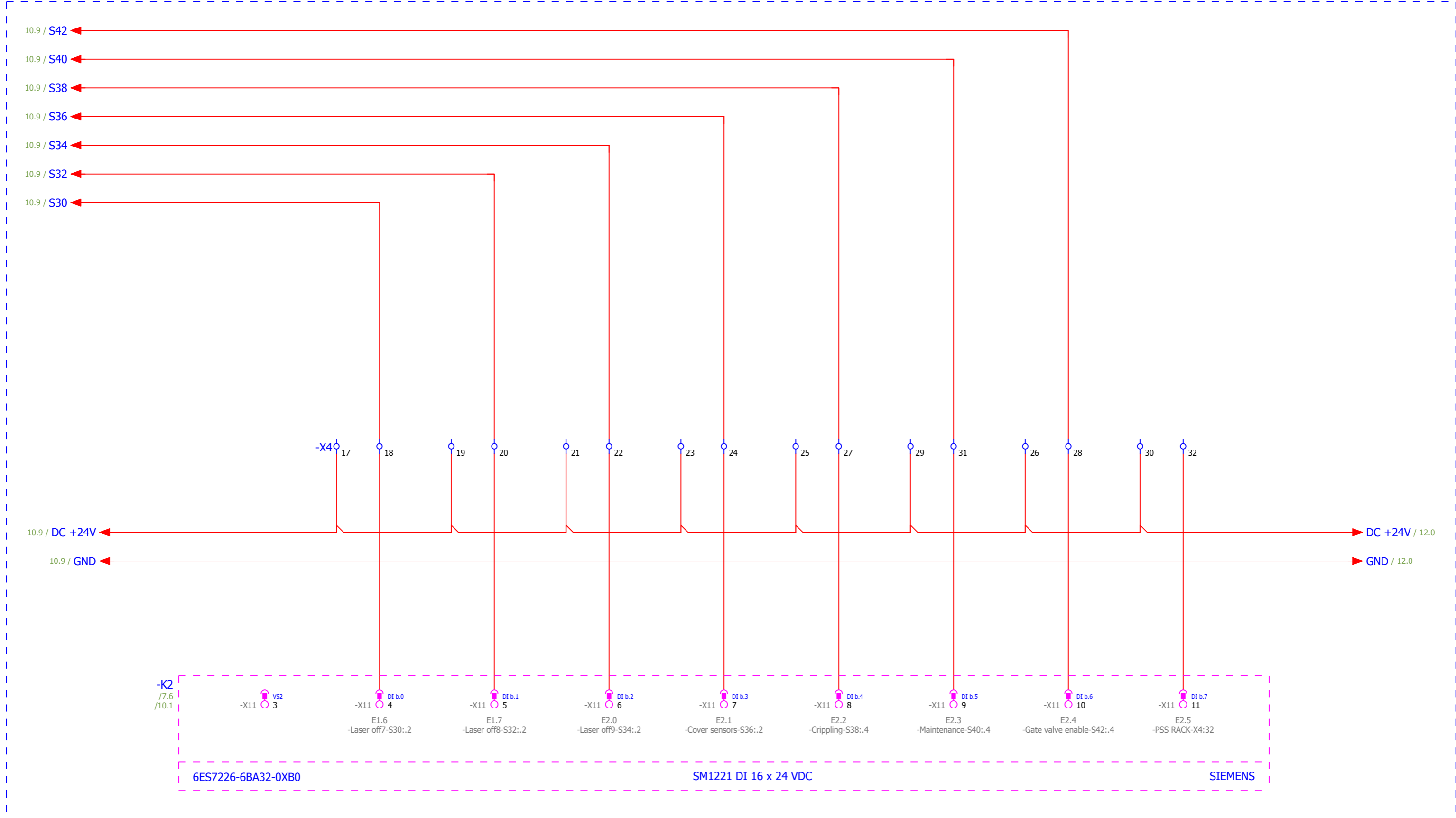
PSS RACK



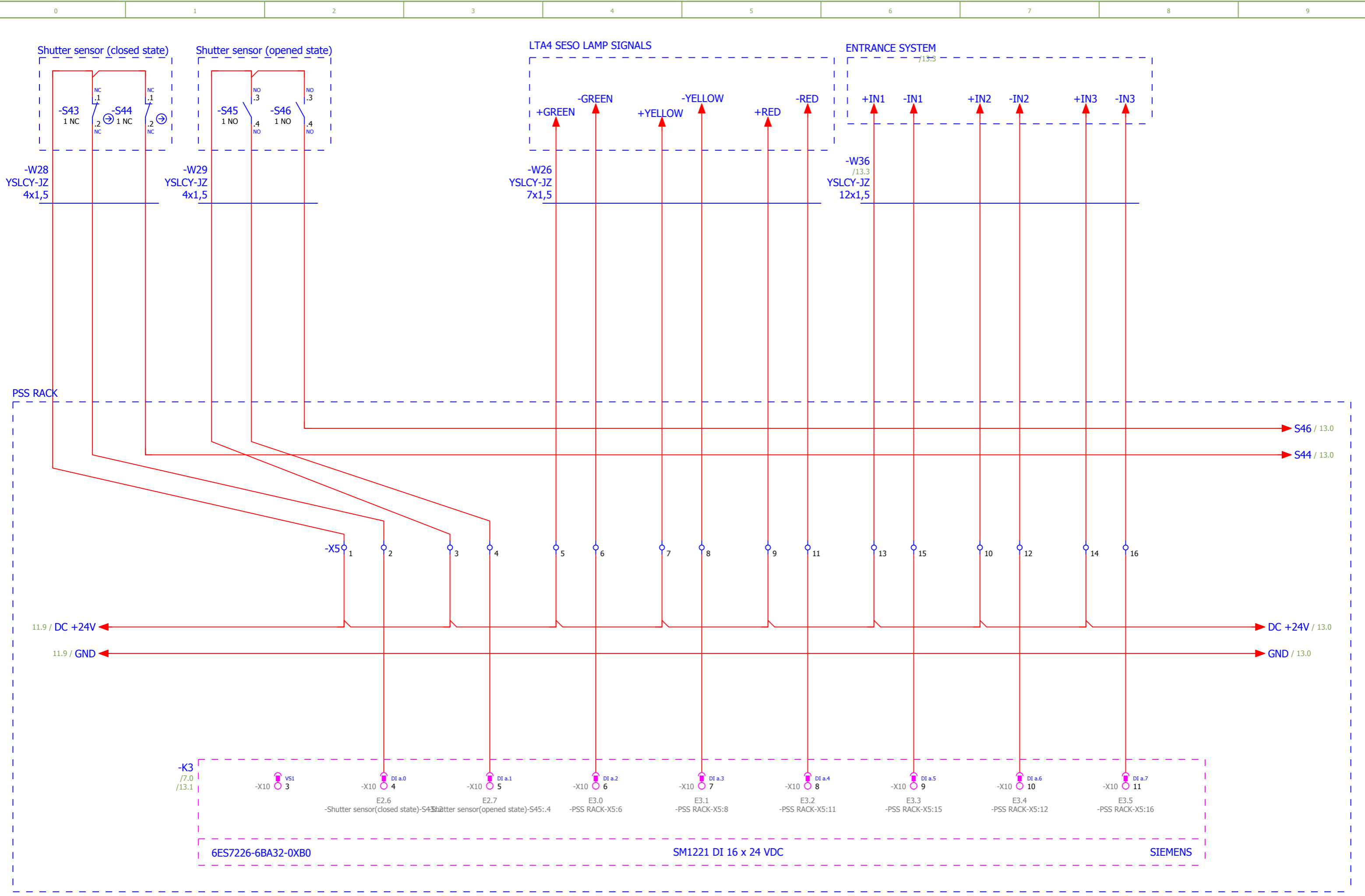
			Dátum	2017.10.18.	PSS HR			INPUT MODULE (1.b)		=				
			Szerk..	Standard								+		
			Lenyomva	eredetileg										Lap
Módosítás	Dátum	Név	eredetileg	Kiegészítette	Pótolta					ELI_EE_2017		Tervlap	9 / 19	

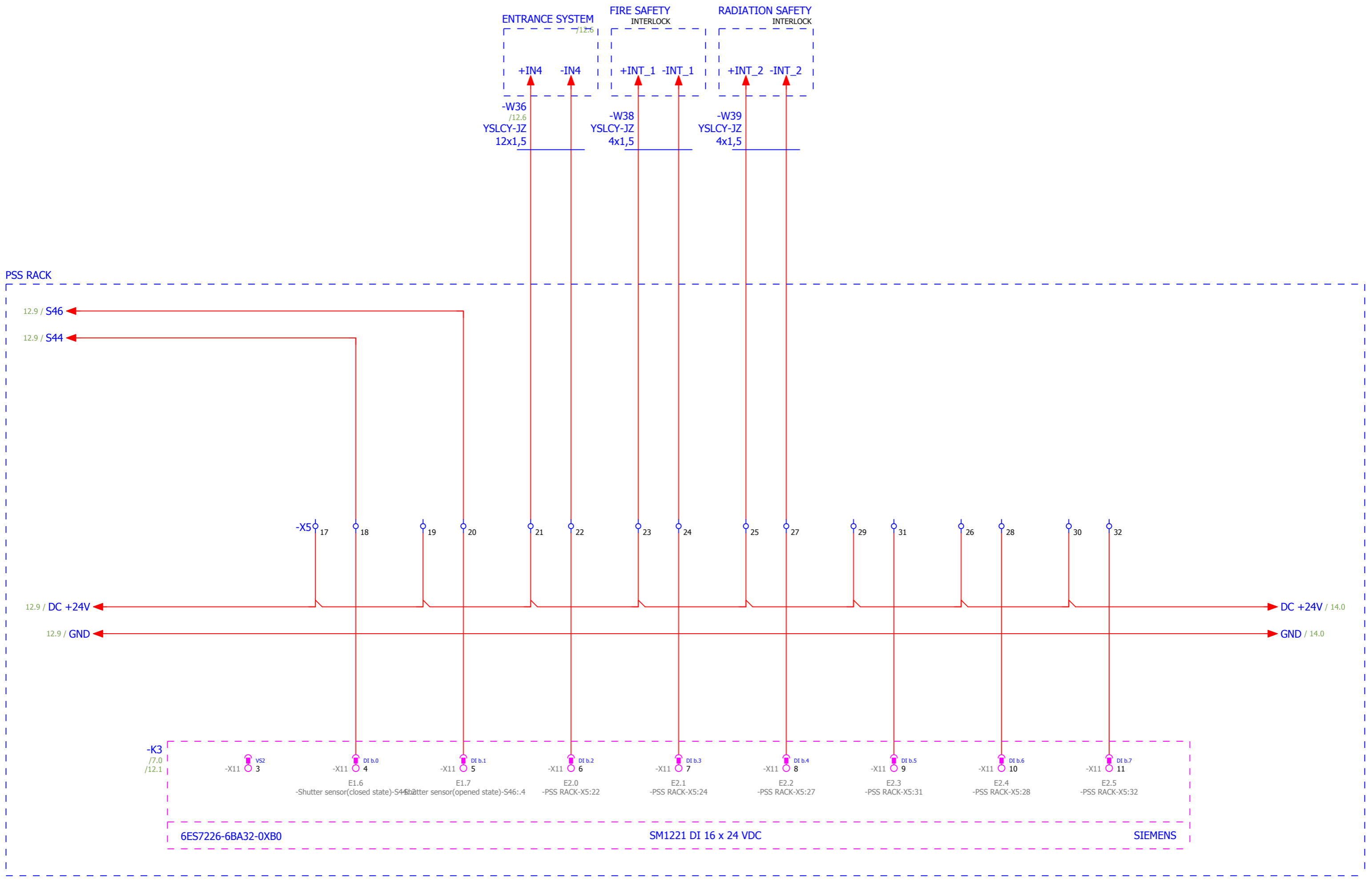


PSS RACK



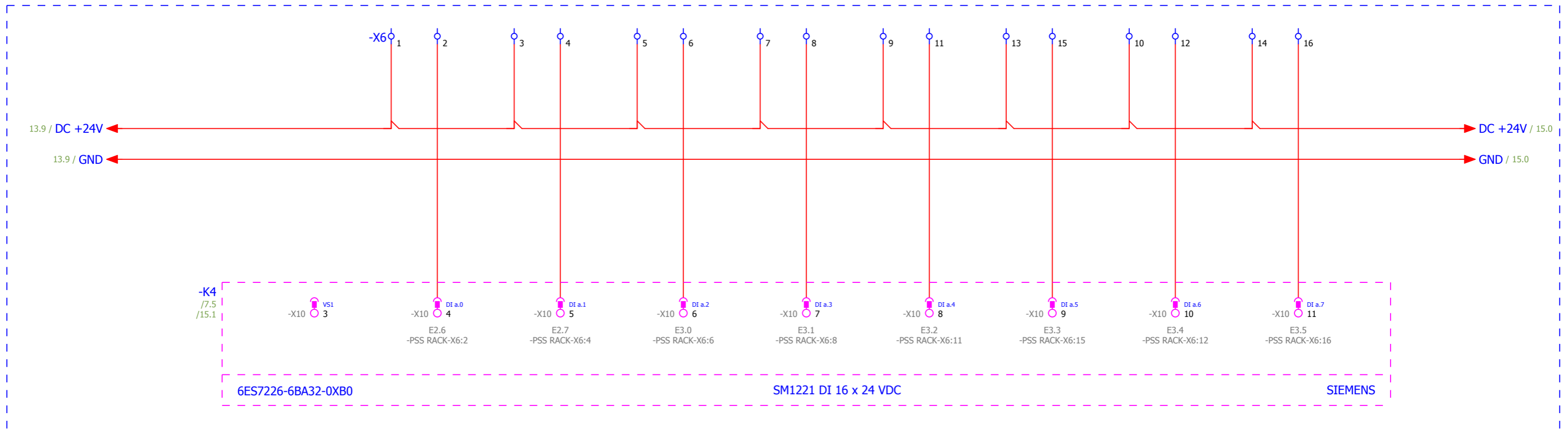
			Dátum	2017.10.18.	PSS HR			INPUT MODULE (2.b)					
			Szerk..	Standard									
			Lenyomva										
Módosítás	Dátum	Név	eredetileg	Kiegészítette	Pótolta								
										=			
										+			
										ELI_EE_2017			
										Lap 11			
										Tervlap 11 / 19			





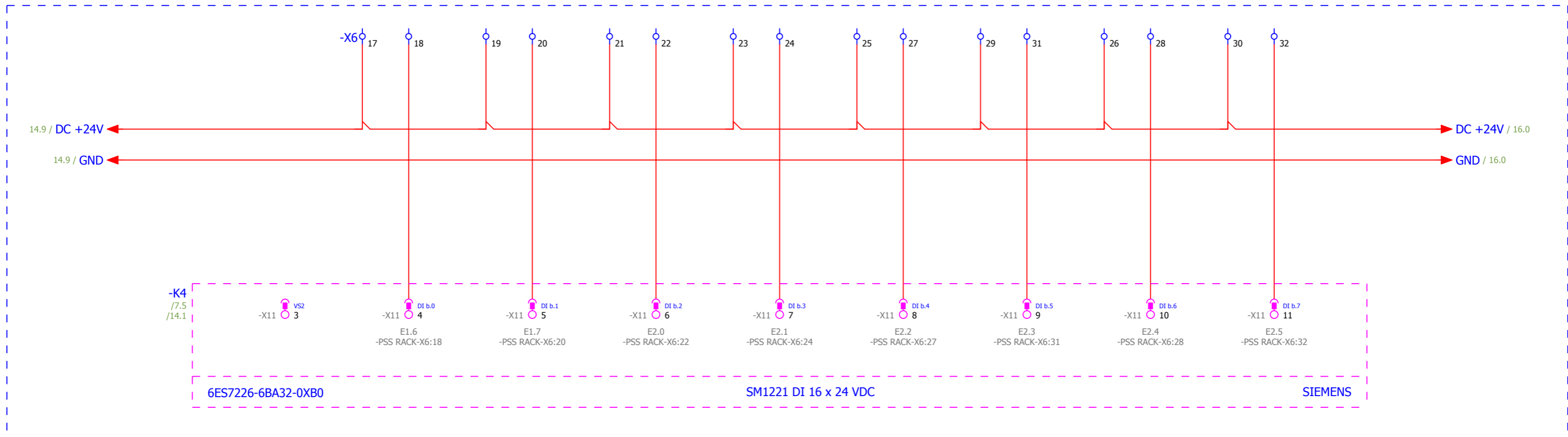
			Dátum	2017.10.18.	PSS HR			INPUT MODULE (3.b)					
			Szerk..	Standard									
			Lenyomva										
Módosítás	Dátum	Név	eredetileg	Kiegészítette	Pótolta					=			
										+ ELI_EE_2017			
										Lap 13			
										Tervlap 13 / 19			

PSS RACK



			Dátum	2017.10.18.	PSS HR			INPUT MODULE (4.a)		=			
			Szerk..	Standard								+	
			Lenyomva	eredetileg									
Módosítás	Dátum	Név	eredetileg	Kiegészítette	Pótolta					ELI_EE_2017			
										Lap 14			
										Tervlap 14 / 19			

PSS RACK



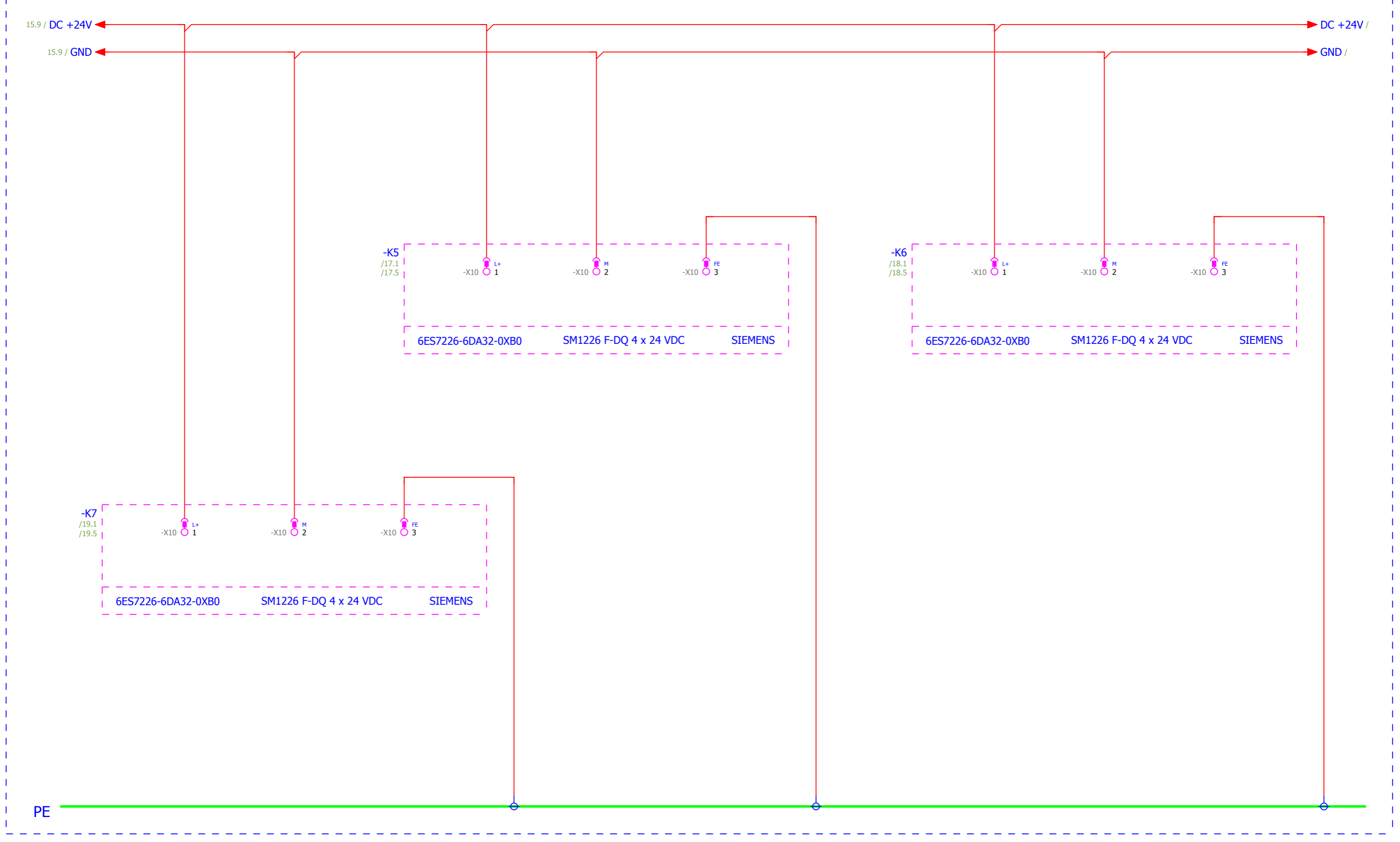
Dátum	2017.10.18.	PSS HR	
Szerk..	Standard		
Lenyomva			
Módosítás	Dátum	Név	eredetileg
		Kiegészítette	Pótolta



INPUT MODULE (4.b)

=	
+	
ELI_EE_2017	Lap 15
	Tervlap 15 / 19

PSS RACK



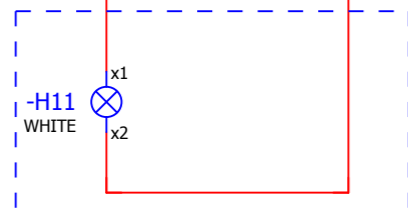
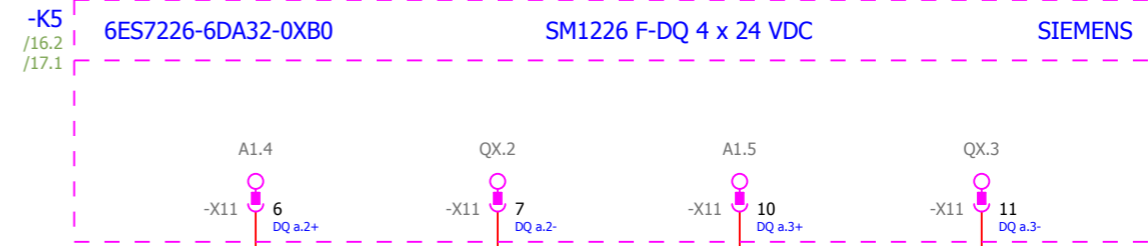
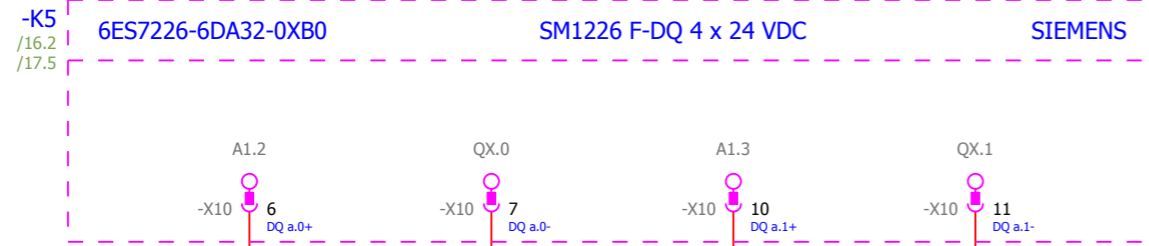
Dátum	2017.10.18.
Szerk..	Standard
Lenyomva	
Módosítás	Dátum
Név	
eredetileg	

PSS HR
Kiegészítette
Pótolta

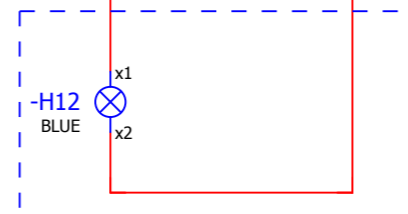


SUPPLY OF OUTPUT MODULES

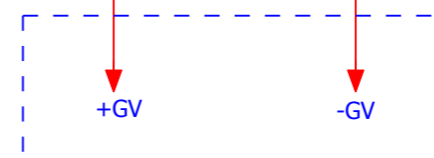
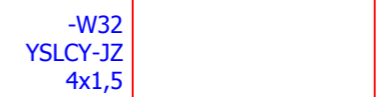
=	
+	
ELI_EE_2017	Lap 16
	Tervlap 16 / 19



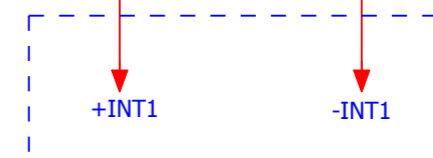
Crippling
/10.4



Indicator of cover sensors



LTA4 SESO GATE VALVE ENABLE



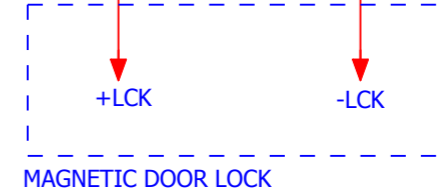
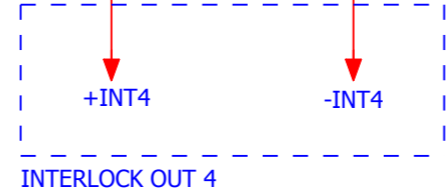
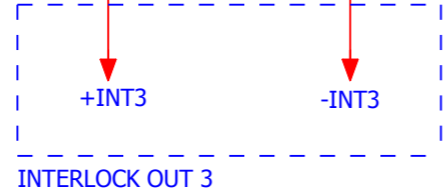
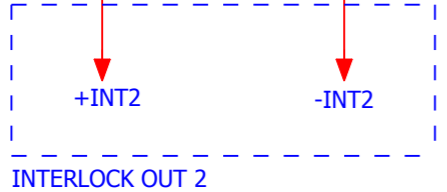
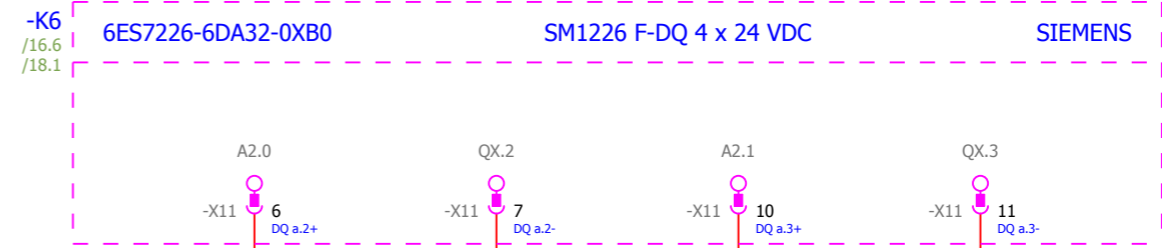
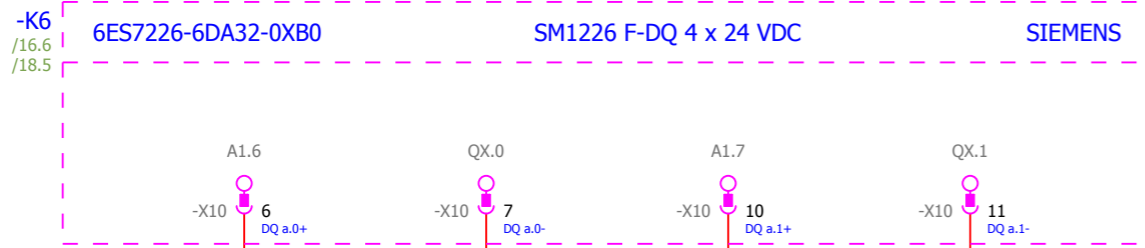
INTERLOCK OUT 1

Dátum	2017.10.18.	PSS HR			
Szerk..	Standard				
Lenyomva					
Módosítás	Dátum	Név	eredetileg	Kiegészítette	Pótolta

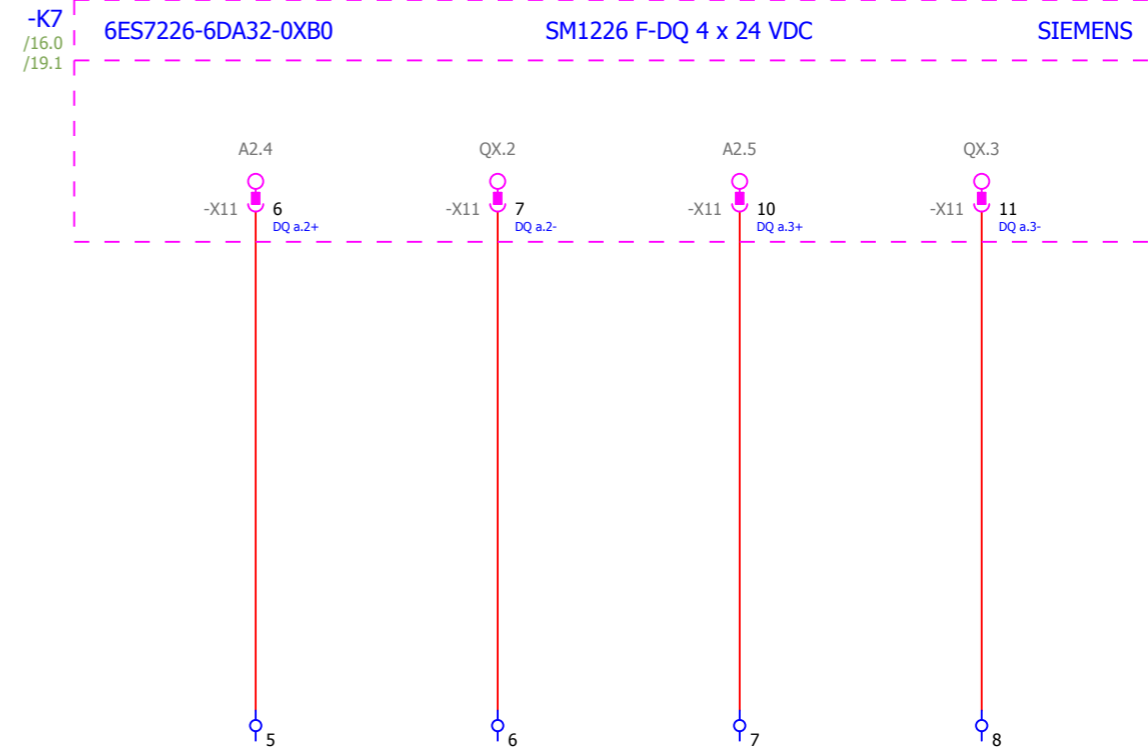
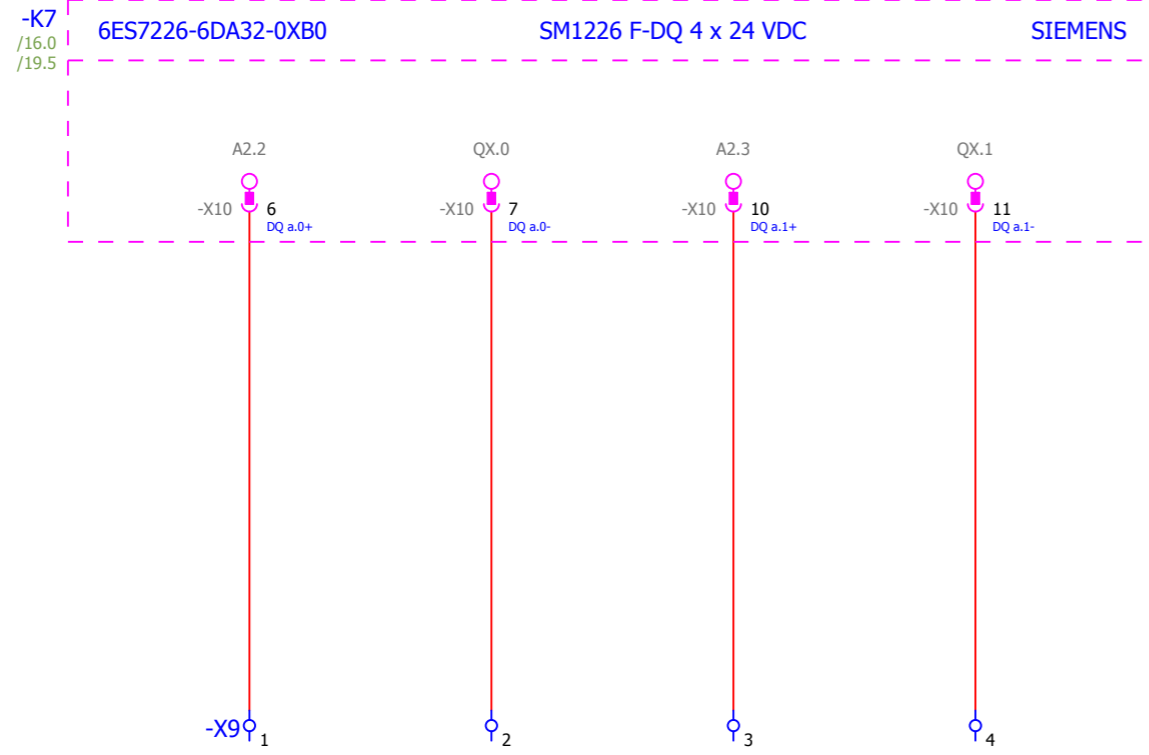


OUTPUT MODULE (1)

=	
+	
ELI_EE_2017	Lap 17
	Tervlap 17 / 19



			Dátum	2017.10.18.	PSS HR		OUTPUT MODULE (2)		=	
			Szerk..	Standard					+	
			Lenyomva							
Módosítás	Dátum	Név	eredetileg		Kiegészítette	Pótolta			ELI_EE_2017	Lap 18
									Tervlap 18 / 19	



Dátum	2017.10.18.
Szerk..	Standard
Lenyomva	
Módosítás	Dátum
Név	
eredetileg	

PSS HR	
Kiegészítette	Pótolta



OUTPUT MODULE (3)

		=
		+
ELI_EE_2017	Lap	19
	Tervlap	19 / 19